

ERRATA for
PE Electrical and Computer: Computer Engineering Practice Exam
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Revisions are shown in red.

Question 38, p. 27:

Line 2 of the question should read as follows:

$$D(A,B,C) = \overline{(A+B)}C$$

Question 49, p. 37:

The question should read as follows:

The synchronous digital circuit below has input CLK and output $Q_1Q_2Q_3$. Assume positive logic. If the current state of the synchronous circuit is $Q_1Q_2Q_3 = 100$, the next state will be: